

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:
Michael C. Stolowitz

Application No.:

Filed:

For: **METHOD AND APPARATUS FOR
SYNCHRONIZING DATA FROM
ASYNCHRONOUS DISK DRIVE DATA
TRANSFERS**

Docket No. 44541/6:2

Date: April 8, 2004

INFORMATION DISCLOSURE STATEMENT

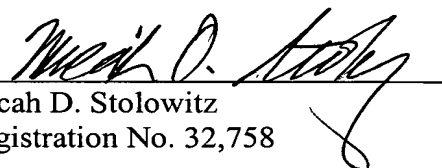
TO THE COMMISSIONER FOR PATENTS:

1. Pursuant to the duty of disclosure, applicant presents for the Examiner's consideration the documents listed on the accompanying Form PTO-1449.
 - ☐ Copies of listed documents are enclosed. (37 CFR § 1.98(a)).
 - ☒ Copies of listed U.S. patent documents are omitted because this application was filed after June 30, 2003 and is, thus, subject to image file wrapper processing. Copies of listed foreign patent documents and non-patent literature are enclosed.
 - ☐ Copies of the documents listed on sheet(s) _____ of Form PTO-1449 (or equivalent) are omitted because (1) they are already of record in U.S. Patent Application No. _____, filed _____, on which this application relies for an earlier filing date under 35 U.S.C. § 120; and (2) any information disclosure statement filed in the prosecution of Application No. _____, complies with 37 CFR §§ 1.98(a) through (c). (37 C.F.R. § 1.98(d))
2. ☐ The Examiner's attention is directed to the enclosed copy of copending U.S. Patent Application No. _____, filed _____, for _____, which is cited in this application.
3. This information disclosure statement is being submitted (check box a., b., or c.):
 - a. ☒ Within three months of the filing date of a national application or entry of the national stage in an international application; or before the mailing of a first Office action on the merits; or before the mailing of a first Office action after the filing of a request for continued examination under 37 CFR 1.114. (No statement under 37 CFR 1.97(e) is required.); or

- b. ☐ After the period set forth in paragraph 3a, but before the mailing date of either a final action, a notice of allowance, or an action that otherwise closes prosecution in the application. (Check box i. or ii.)
- i. ☐ A \$180.00 information disclosure statement submission fee set forth in 37 CFR 1.17(p) is enclosed, or
- ii. ☐ A statement specified by 37 CFR 1.97(e) is set forth below; or
- c. ☐ After the mailing date of a final action or notice of allowance and on or before payment of the issue fee. A statement specified by 37 CFR 1.97(e) is set forth below. Enclosed is a \$180.00 information disclosure statement processing fee set forth in 37 CFR 1.17(p).
4. If a statement specified by 37 CFR 1.97(e) is required, the attorney or agent signing below hereby states that:
- ☐ each item of information contained in the information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the information disclosure statement; or
- ☐ no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.
5. ☐ A concise explanation of the relevance of each document not in the English language and/or selected documents in the English language is set forth be

Respectfully submitted,

NetCell Corp.

By: 
Micah D. Stolowitz
Registration No. 32,758

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FORM PTO-1449
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.:
44541/6:2

APPLICATION NO.:

INFORMATION DISCLOSURE CITATION
(Uses several sheets if necessary)

APPLICANT: Michael C. Stolowitz

FILING DATE:

GROUP:

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	5	9	6	4	8	6	6		10/12/99	Durham, <i>et al.</i>	712	200	
	AB	5	7	7	1	3	7	2		6/23/98	Pham, <i>et al.</i>	395	551	
	AC	5	4	9	1	8	0	4		2/13/96	Heath, <i>et al.</i>	395	275	
	AD	5	4	7	1	5	8	1		11/28/95	Munier, <i>et al.</i>	395	250	
	AE	5	4	2	8	6	4	9		6/27/95	Cecchi	375	372	
	AF	5	3	9	2	4	2	5		2/21/95	Elliott, <i>et al.</i>	395	575	
	AG	5	2	6	8	5	9	2		12/7/93	Bellamy, <i>et al.</i>	307	43	
	AH	5	1	8	5	8	6	2		2/9/93	Casper, <i>et al.</i>	395	250	
	AI	5	1	5	1	9	7	7		9/29/92	Fredericks, <i>et al.</i>	395	200	
	AJ	5	0	3	8	3	2	0		8/6/91	Heath, <i>et al.</i>	364	900	
	AK	5	0	0	3	5	5	8		3/26/91	Gregg	375	108	
	AL	4	5	1	4	8	2	3		8/30/85	Mendelson, <i>et al.</i>	364	900	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
														YES	NO
	AM														

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	EDN Access; for Design, by Design: "Embedded Systems," January 18, 1996, published on the Internet by <i>EDN Magazine</i> at < http://www.e-insite.net/ednmag/archives/1996/011896/02prod4.htm >.
	AO	EDN Access; for Design by Design: "Easy-to-Use Flash-Memory Modules Emulate Disk Drives," September 1, 1994, by Gary Legg. Published on the Internet by <i>EDN Magazine</i> at < http://www.e-insite.net/ednmag/archives/1994/090194/18df2.htm >.
	AP	Silicon Valley Computer advertisement dated January 21, 1994, by Norm Towson.
	AR	"Silicon Valley Computer, IDE Mirror Card, Model ADP104/ADP108," User Manual, Part No. 113, Copyright 1992 by Silicon Valley Computer. (Cover page, table of contents, introduction, and pages 8-16.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AS	6	2	3	7	0	5	2	5/22/01	Stolowitz	710	61	
	AT	6	0	9	8	1	1	4	8/1/00	McDonald, <i>et al.</i>	710	5	
	AU	6	0	1	8	7	7	8	1/25/00	Stolowitz	710	61	
	AV	5	5	8	1	7	1	5	12/3/96	Verinsky, <i>et al.</i>	395	309	
	AW	5	8	9	0	0	1	4	3/30/99	Long	395	828	
	AX	5	7	9	4	0	6	3	8/11/98	Favor	395	800.23	
	AY												

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

AZ	Sven Schulz, "Elegant Pair Skating," Promise FastTrak, EIDE RAID Adapter, English translation of a German article (also submitted herewith) published in <i>C'T Magazin Für Computer Technik</i> , June 1998, page 116, XP002216756, Hanover Germany.
BA	5 January 2001 International Search Report for international application number PCT/US00/26343.
BB	Jin, Hai, et al., "Improving Partial Stripe Write Performance in RAID Level 5." Proceedings of the 1998 Second IEEE International Caracas Conference on Devices, Circuits and Systems, 1998. Pages 396-400.
BC	Sahai, Anil K., "Performance Aspects of RAID Architectures." Performance, Computing and Communications Conference, 1997. IPCCC 1997. IEEE International. Pages 321-327.
BD	Zabback, P., et al., "The RAID Configuration Tool," Third International Conference on High Performance Computing, 1996. Pages 55-61.
BE	Cheng, A.M.K., et al., "Improving the I/O Performance of Real-Time Database Systems with Multiple-Disk Storage Structures." Proceedings of the 1996 International Conference on Parallel Processing, 1996. Vol. 3., Pages 204-211.
BF	Feng, Dan, et al., "Performance Analysis of RAID for Different Communication Mechanism Between RAID Controller and String Controllers." IEEE Transactions on Magnetics. Vol. 32, No. 5. September 1996. Pages 3890-3892.
BG	Menon, Jai, et al., "The Architecture of a Fault-Tolerant Cached RAID Controller." Proceedings of the 20th Annual International Symposium on Computer Architecture, 1993. Pages 76-86.
BH	Specification entitled "PCI IDE Controller Specification" (revision 1.0, dated 3/4/94) (anonymous).

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	BI						
	BJ						
	BK						
	BL						
	BM						
	BN						
	BO						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	BP	Specification entitled "Programming Interface for Bus Master IDE Controller" (revision 1.0, dated 5/16/94), by Brad Hosler, Intel Corporation.
	BR	Specification entitled "SATA PHY Interface Specification (SAPIS). Draft rev. 0.90, February 8, 2002. Copyright Intel Corporation.
	BS	
	BT	
	BU	
	BV	
	BW	
	BX	
	BY	

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